

Refine Search

Search Results -

| Terms | Documents |
|----------------------------------|-----------|
| L16 and (FCAL or fibre or fiber) | 0 |

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L17

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, December 09, 2004 [Printable Copy](#) [Create Case](#)

| <u>Set</u> <u>Name</u> | <u>Query</u> | <u>Hit</u> <u>Count</u> | <u>Set</u> <u>Name</u> result set |
|---|--|----------------------------|--|
| side by side | | | |
| <i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> | | | |
| <u>L17</u> | L16 and (FCAL or fibre or fiber) | 0 | <u>L17</u> |
| <u>L16</u> | L15 and ((block or memory or cache or storage) near8 allocat\$4) | 2 | <u>L16</u> |
| <u>L15</u> | L12 and ((memory or storage or cache) near8 (segment\$3 or partition\$4 or section\$4 or area or divi\$4 or part) near6 mirror\$4) | 23 | <u>L15</u> |
| <u>L14</u> | L13 and ((memory or storage or cache) near8 (segment\$3 or partition\$4 or section\$4 or area or divi\$4 or part) near6 mirror\$4) | 0 | <u>L14</u> |
| <u>L13</u> | L12 and (FCAL or fibre or fiber) | 4 | <u>L13</u> |
| <u>L12</u> | L11 and ((controller or processor or cpu) with (memory or cache or storage)) | 727 | <u>L12</u> |
| <u>L11</u> | ((mirror\$4 or redundant\$3) near4 (memory or segment or storage)) | 6811 | <u>L11</u> |
| <i>DB=PGPB,USPT; PLUR=YES; OP=OR</i> | | | |
| <u>L10</u> | L9 and ((block or memory or cache or storage) near8 allocat\$4) | 69 | <u>L10</u> |
| <u>L9</u> | L8 and ((memory or storage or cache) near8 (segment\$3 or partition\$4 or section\$4 or area or divi\$4 or part) near6 mirror\$4) | 142 | <u>L9</u> |

| | | | |
|-----------|--|-------|-----------|
| <u>L8</u> | L7 and (FCAL or fibre or fiber) | 1832 | <u>L8</u> |
| <u>L7</u> | L6 and ((controller or processor or cpu) with (memory or cache or storage)) | 7198 | <u>L7</u> |
| <u>L6</u> | ((mirror\$4 or redundant\$3) near4 (memory or segment or storage)) | 15187 | <u>L6</u> |
| | <i>DB=USPT; PLUR=YES; OP=OR</i> | | |
| <u>L5</u> | l4 and (FCAL or fibre or fiber) | 3 | <u>L5</u> |
| <u>L4</u> | L3 and l2 | 6 | <u>L4</u> |
| <u>L3</u> | L1 and ((controller or processor or cpu) with (memory or cache or storage)) | 12 | <u>L3</u> |
| <u>L2</u> | l1 and ((mirror\$4 or redundant\$3 or secondary) near4 (memory or segment or storage)) | 6 | <u>L2</u> |
| <u>L1</u> | ((6188973 or 6237112 or 6243790 or 5835700 or 6266721 or 6073209 or 6282910 or 6260120 or 6038689 or 5987622 or 5822777 or 5816649 or 5513314 or 5109318 or 6815371 or 5184281 or 6278609 or 6282096 or 6282094 or 5996089 or 5832222 or 6148414).pn.) | 22 | <u>L1</u> |

END OF SEARCH HISTORY